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Application No.: 10/786,703

AUG 02 2005

Docket No.: JCLA12969

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Chih-Feng Huang et al.)	Examiner	: QUINTO, KEVIN V
Serial No. : 10/786,703)	Art Unit	: 2826
Filed : 02/24/2004)	Docket No.	: JCLA 12969
For : HIGH VOLTAGE LDMOS)		
TRANSISTOR HAVING AN)		
ISOLATED STRUCTURE)		

Reez
BS

AMENDMENT AFTER FINAL

MAIL STOP AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir,

The Office Action dated May 19, 2005 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.

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